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FROM:

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DATE:

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Number of pages with cover page:	11		
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Comments:

PLEASE PROCESS THE ATTACHED.

U.S. Patent Application Serial No. 10/802,549

For: ADJUSTING DIE PLACEMENT ON A SEMICONDUCTOR

WAFER TO INCREASE YIELD

By: Eitan CADOURI

Our Reference: 52432-20006.00

Attached is the following:

- 1. Transmittal (1 page)
- 2. Amendment Under 37 CFR 1.111 (9 pages)

DEC 1 2 2005

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Under the Paperwork Roduction Act to	1885, No 3813006 819 (CC)	Application Number	10/802,549
TRANSMITTAL FORM (to be used for all correspondence after initial thing)		Filing Date	March 16, 2004
		First Named Inventor	Eitan CADOURI
		Group Art Unit	2818
		Examiner Name	Q. Hoang
Total Number of Pages In This Submission 10		Attorney Docket Numb	er 524322000600
	ENCLOS	URES (check all	that apply)
Fee Transmittal Form	Assignment i		After Allowance Communication to Group
Fee Attached	Drawing(s)	•	Appeal Communication to Board of Appeals and Interferences
X Amendment/Reply (9 pages)	Licensing-rel	ated Papers	Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
After Final	Petitlan		Proprietary Information
Affidavits/declaration(s)	Petition to Convert to a Provisional Application		Status Letter
Extension of Time Request	Power of Attorney (1 page) Revocation		X Other Enclosure(s) (please identify below)
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Certified Copy of Priority Document(s)	CD, Number	of CD(s)	
Response to Missing Parts/ Incomplete Application	Remarks		
Response to Missing Parts			
under 37 CFR 1.52 or 1.53			
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Date			
I hereby certify that this correspondence is 6300, on the date shown below.	being facsimile trans	smitted to the Patent and 1	rademark Office, facsimile no. (571) 273-
Dated: December 12, 2005	Signatur	: Valeur Ca	(Valente Cohen)

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Docket No.: 524322000600

(PATENT)

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Dated: _December 12, 2005

Signature: 100 Color (Valerie Cohen)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Eitan CADOURI

Application No.: 10/802,549

02,549 Confirmation No.: 3719

Filed: March 16, 2004 Art Unit: 2818

For: ADJUSTING DIE PLACEMENT ON A Examiner: P. Dang

SEMICONDUCTOR WAFER TO INCREASE

YIELD

AMENDMENT UNDER 37 C.F.R. 1.111

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This is in response to the non-final Office Action dated September 12, 2005 (Paper No. Part of Paper No./Mail Date 090905), for which a response is due on December 12, 2005.

Accordingly, this response is timely filed. Reconsideration and allowance of the pending claims, as amended, in light of the remarks presented herein are respectfully requested.

sf-2003556